

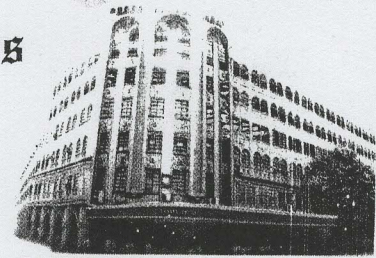
# University of San Jose-Coleto

## Office of the Registrar

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PAASCU ACCREDITED

AUTONOMOUS STATUS

# OFFICIAL TRANSCRIPT OF RECORDS

**OMEGA**

( Last Name )

**REX KENNETH**

( First Name )

**GARCIANO**

( Middle Name )

Course No.	Descriptive Title	Final Grade	Removal Grade	Credit Units
<b><u>B.S.E.E. - 2</u></b>				
<b><u>1st Semester 2013-2014</u></b>				
Filipino 1	Komunikasyon sa Akademikong Pilipino	1.50		3.0
P.E. 3*	Individual/Dual Sports/Games	1.10		2.0
Philo 1	Logic	1.00		3.0
ReEd 3	Church and Sacraments	1.10		3.0
<b><u>B.S.E.E. - 2</u></b>				
<b><u>2nd Semester 2013-2014</u></b>				
Econ 1	Principles of Economics with Taxation & Land Reform	1.20		3.0
EM 22A	Integral Calculus	1.00		4.0
EM 22B	Probability & Statistics	1.20		3.0
Filipino 2	Pagbasa't Pagsulat sa Iba't-ibang Disiplina	1.10		3.0
P.E. 4*	Team Sports/Games	1.20		2.0
PolSc 1	Philippine Gov't & New Constitution Including Public Service	1.00		3.0
ReEd 4	Christian Morality	1.20		3.0
Rizal	Life & Works of Dr. Jose Rizal	1.10		3.0
<b><u>B.S.E.E. - 3</u></b>				
<b><u>1st Semester 2014-2015</u></b>				
ECE 31E	Electronics Circuits and Devices (lec)	1.00		2.0
ECE 31F	Electronics Circuits and Devices (lab)	2.30		1.0
EE 31A	Electrical Circuits 1 (lec)	2.50		3.0
EE 31B	Electrical Circuits 1 (lab)	1.40		1.0
EE 31C	Electromagnetics	2.40		3.0
EM 31A	Differential Equations	1.30		3.0
English 3	Speech Improvement and Oral Communications	1.20		3.0
ES 03A	Engineering Economy	1.60		3.0
ME 04C	Fundamentals of Materials Science and Engineering	1.30		3.0
Mech 01A	Statics of Rigid Bodies	1.00		3.0
<b><u>B.S.E.E. - 3</u></b>				
<b><u>2nd Semester 2014-2015</u></b>				
ECE 32E	Electronic Circuits Analysis and Design (lec)	2.40		2.0
ECE 32F	Electronic Circuit Analysis and Design (lab)	1.00		1.0
EE 32A	Circuits 2 (lec)	2.60		3.0
EE 32B	Electrical Circuits 2 (lab)	1.30		1.0

Purpose : **For Employment**

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Date Issued : **August 7, 2017**

Checked By : **Rolando S. Mantalaba**

Prepared By : **Violeta P. Lucañas**

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